

FIG. 1

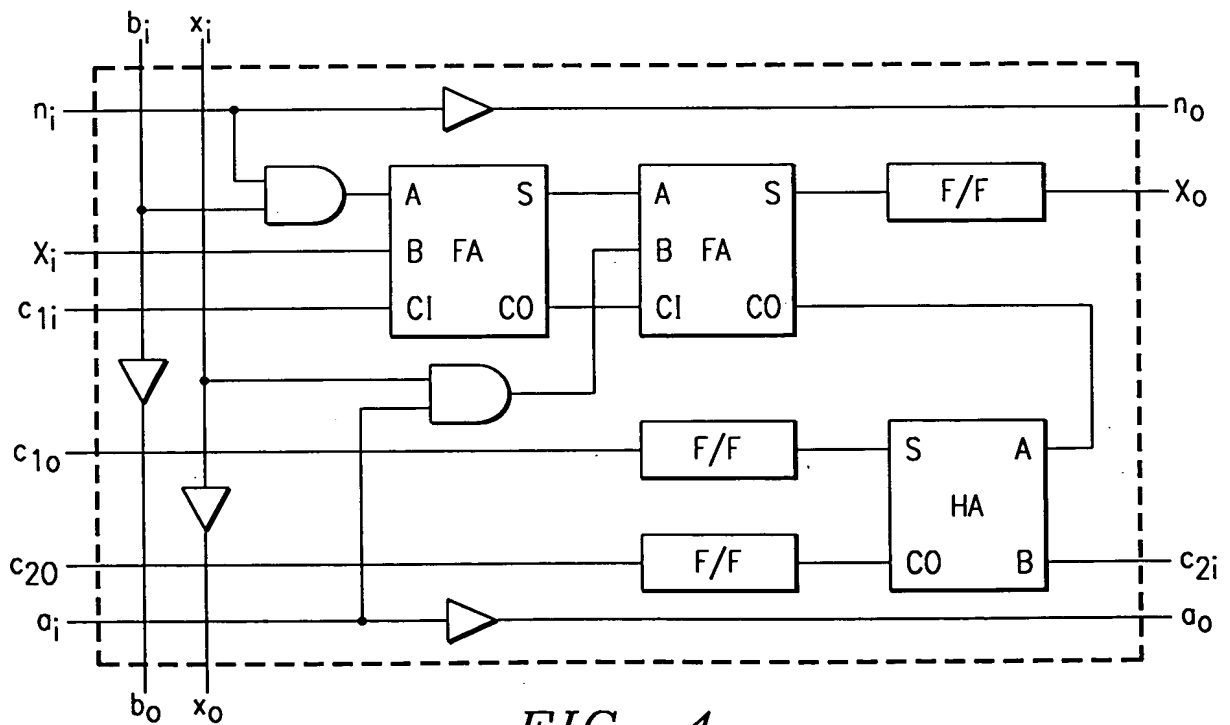
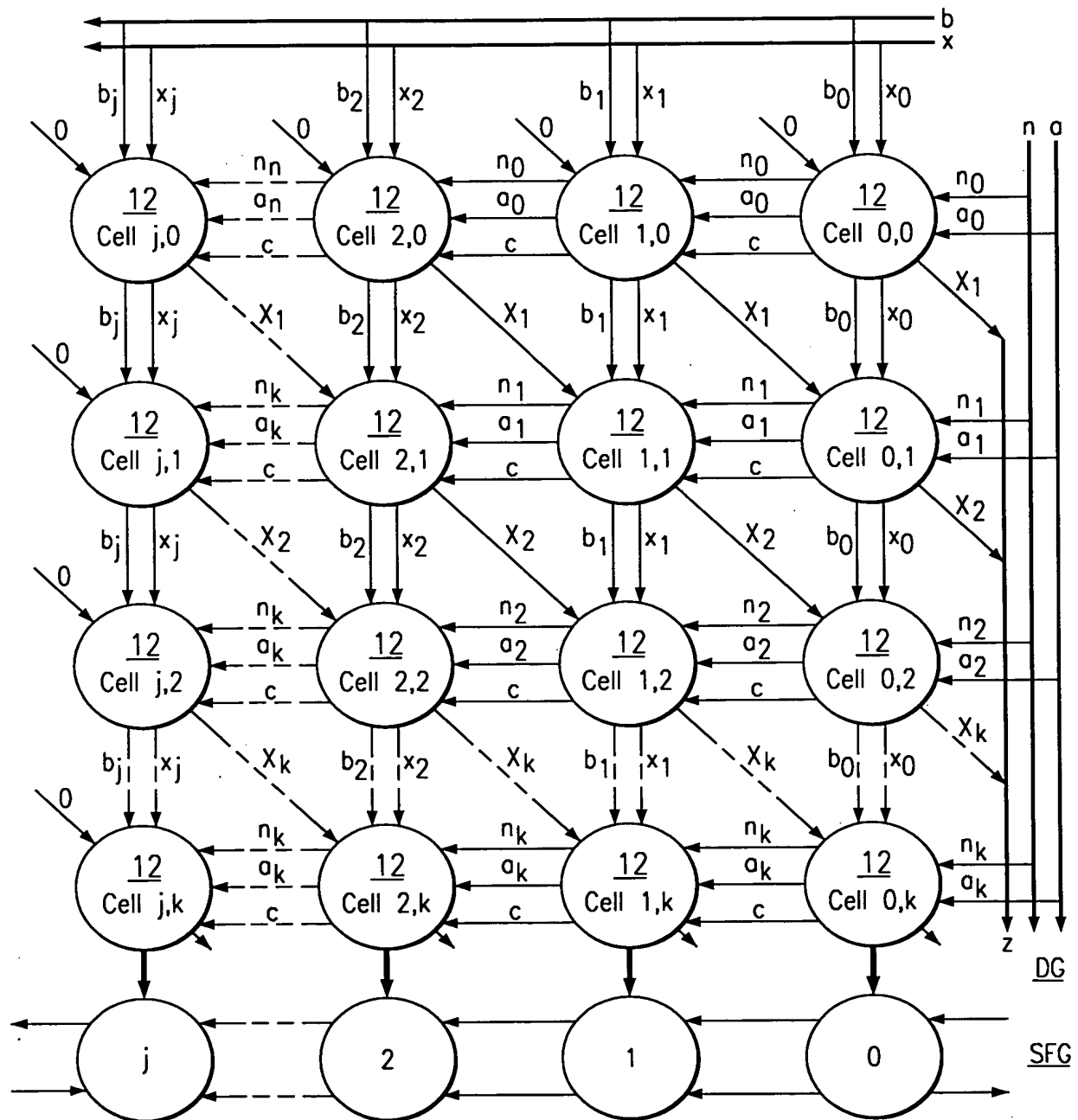


FIG. 4



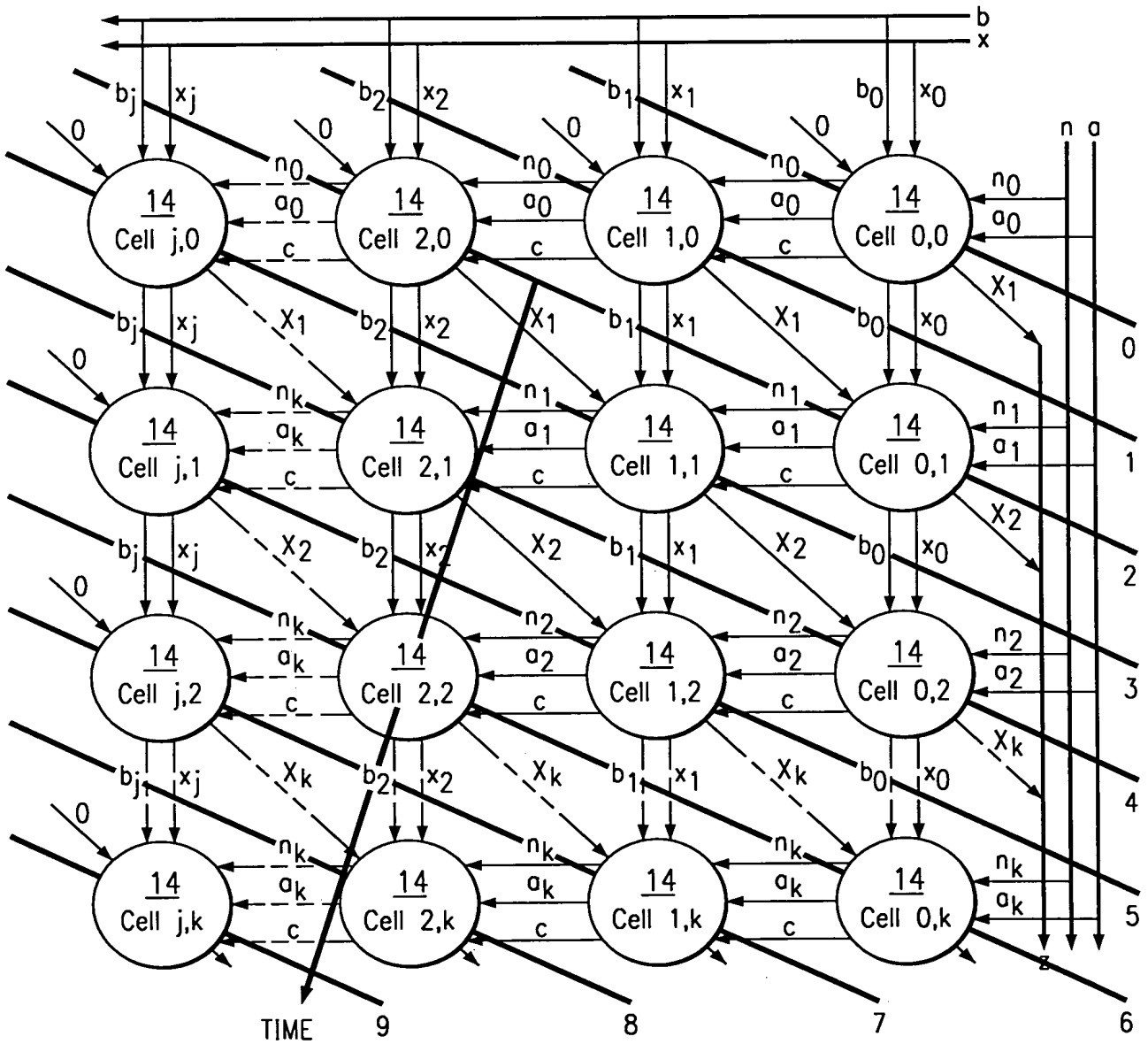


FIG. 3

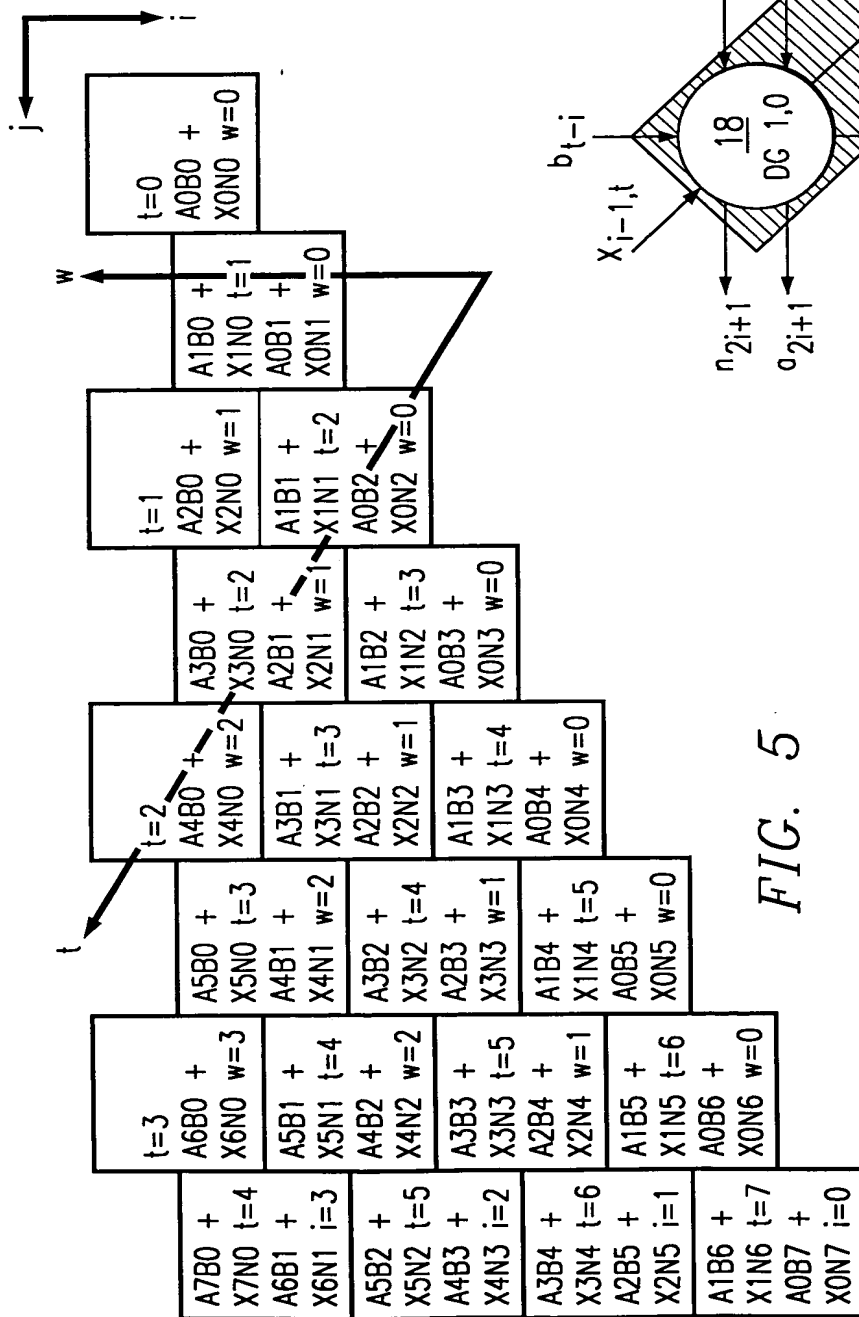


FIG. 5

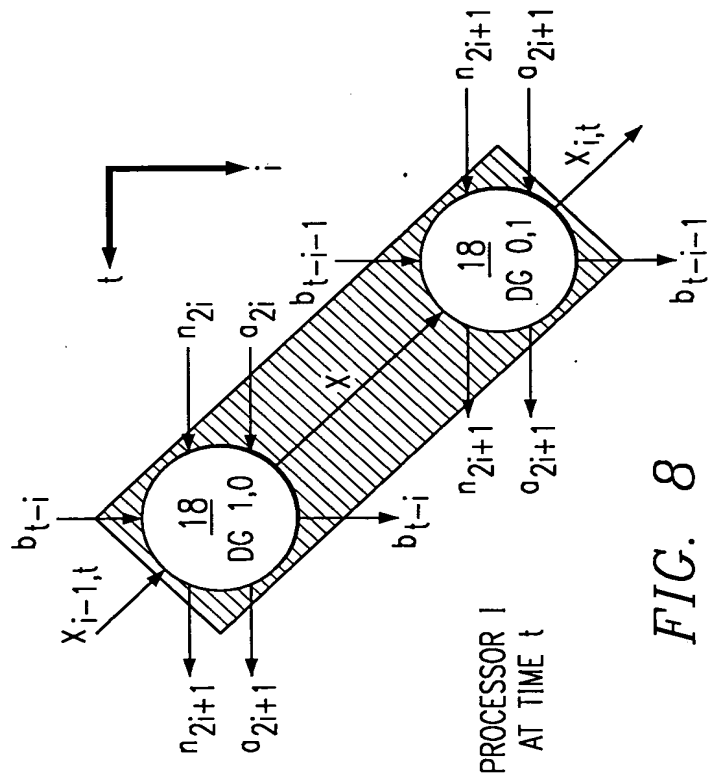


FIG. 8

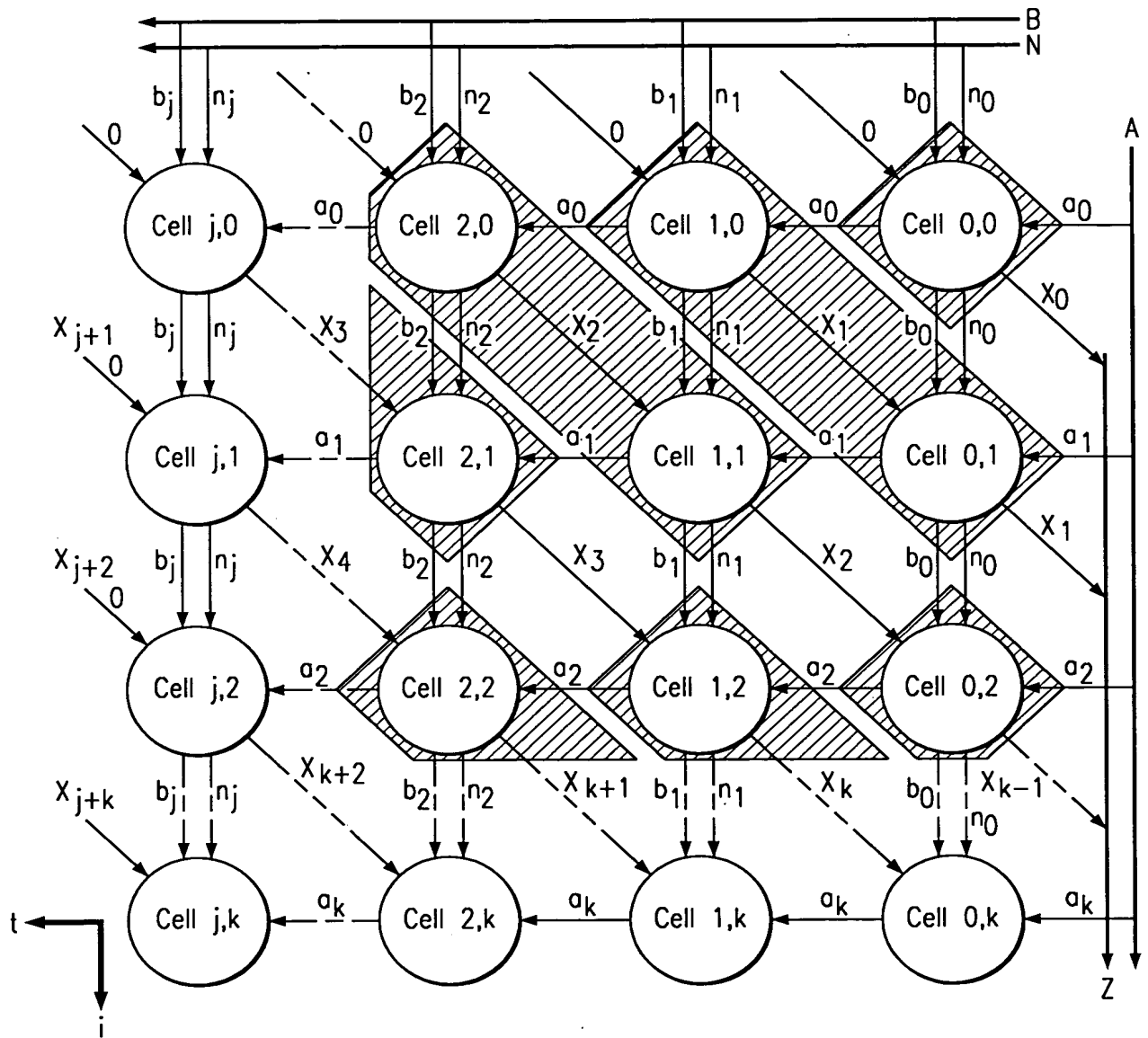


FIG. 6

0
1
2
3
4
5
6
7
8
9
A
B
C
D
E
F
G
H
I
J
K
L
M
N
O
P
Q
R
S
T
U
V
W
X
Y
Z



FIG. 7

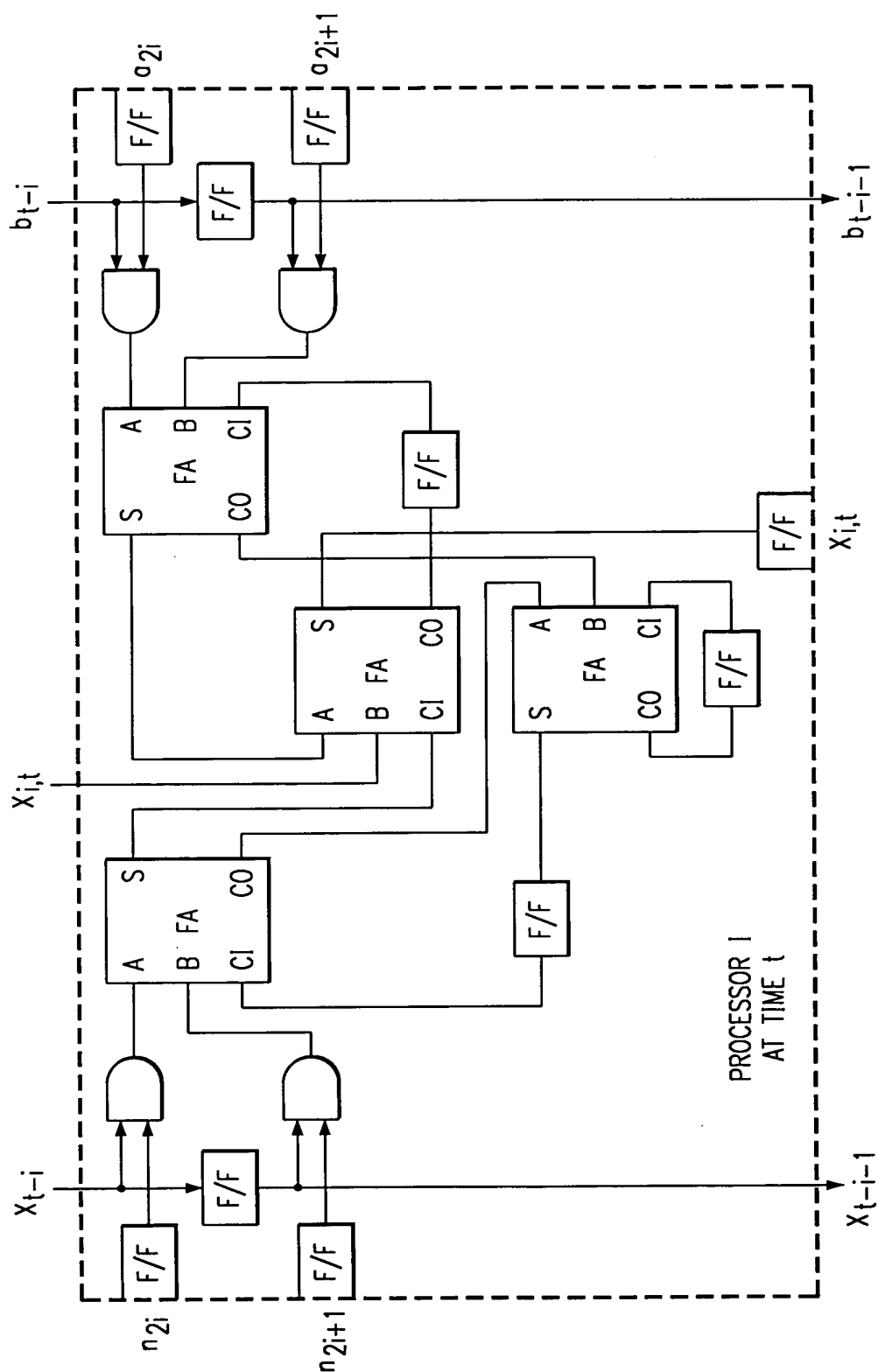


FIG. 9

Process TypicalBinaryMultiply (A,B)

Z:=0;

For i in 0 to n-1 loop

c := 0;

For j in 0 to n-1 loop

$Z_{i+j} := (Z_{i+j} + A_i * B_j + c) \text{ mod } 2;$

$c := (Z_{i+j} + A_i * B_j + c) \text{ div } 2;$

end loop;

for j in n to 2n-1 loop

$Z_j := (Z_j + c) \text{ mod } 2;$

$c := (Z_j + c) \text{ div } 2;$

end loop;

transmit Z_i ; ———→ Z_i ;

end loop;

BITS OF Z FEED
SEQUENTIALLY
ACROSS

FIG. 10

Process Reduce(Z_i , N)

c := 0;

For i in 0 to n-1 loop

wait for Z_i ;

$X_i := Z_i$;

$X_i := (X_i + x * N_i + c) \text{ mode } 2;$

$c := (X_i + x * N_i + c) \text{ div } 2;$

end loop;

X := X/2;

For i in 1 to k-1 loop

x = X mod 2; c := 0;

For j in 0 to n-1 loop

$X_j := (X_j + x * N_j + c) \text{ mod } 2;$

$c := (X_j + x * N_j + c) \text{ div } 2;$

end loop;

X = X/2;

end loop;

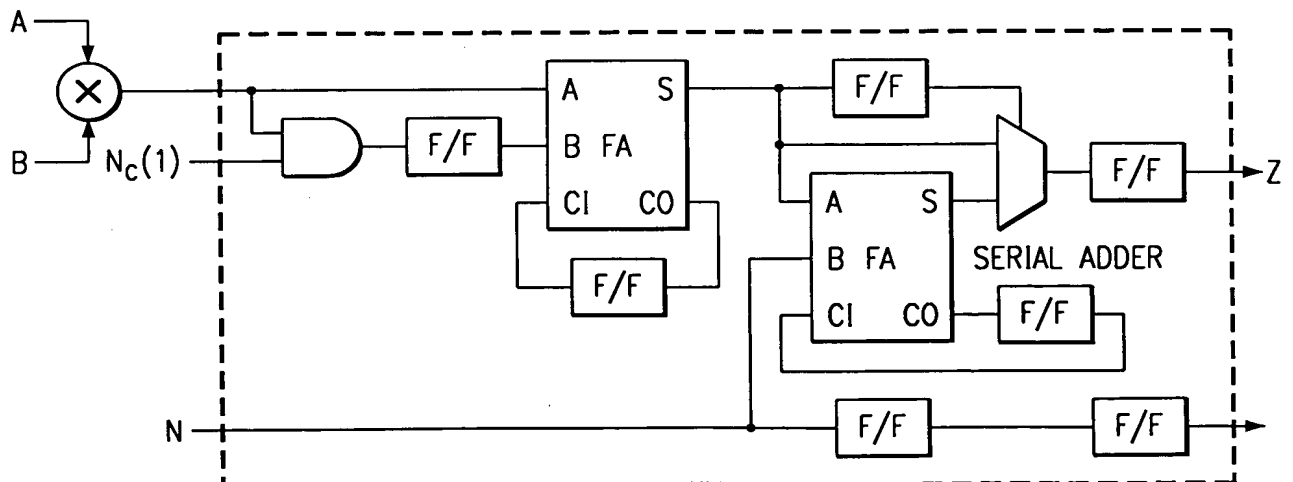
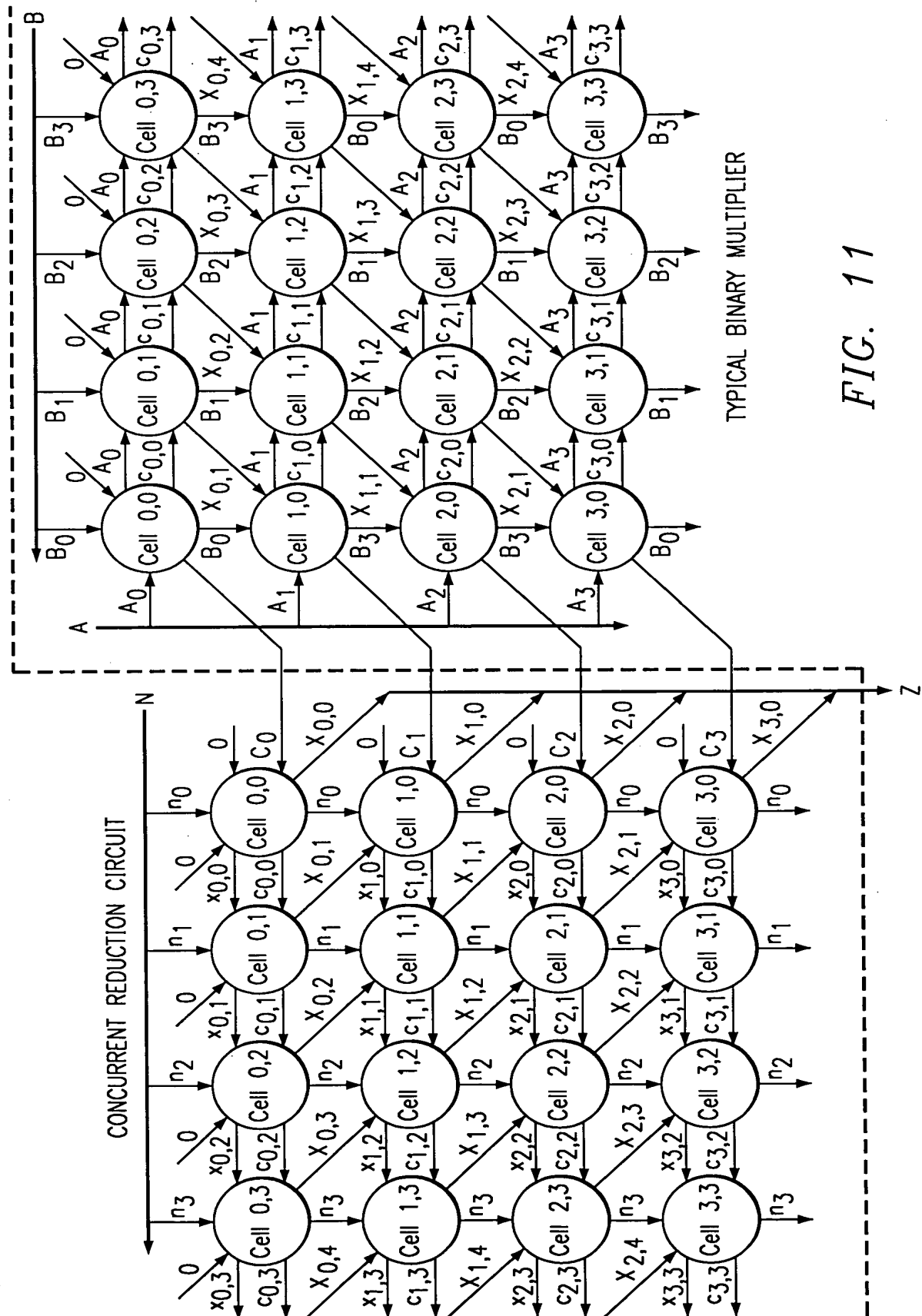


FIG. 18



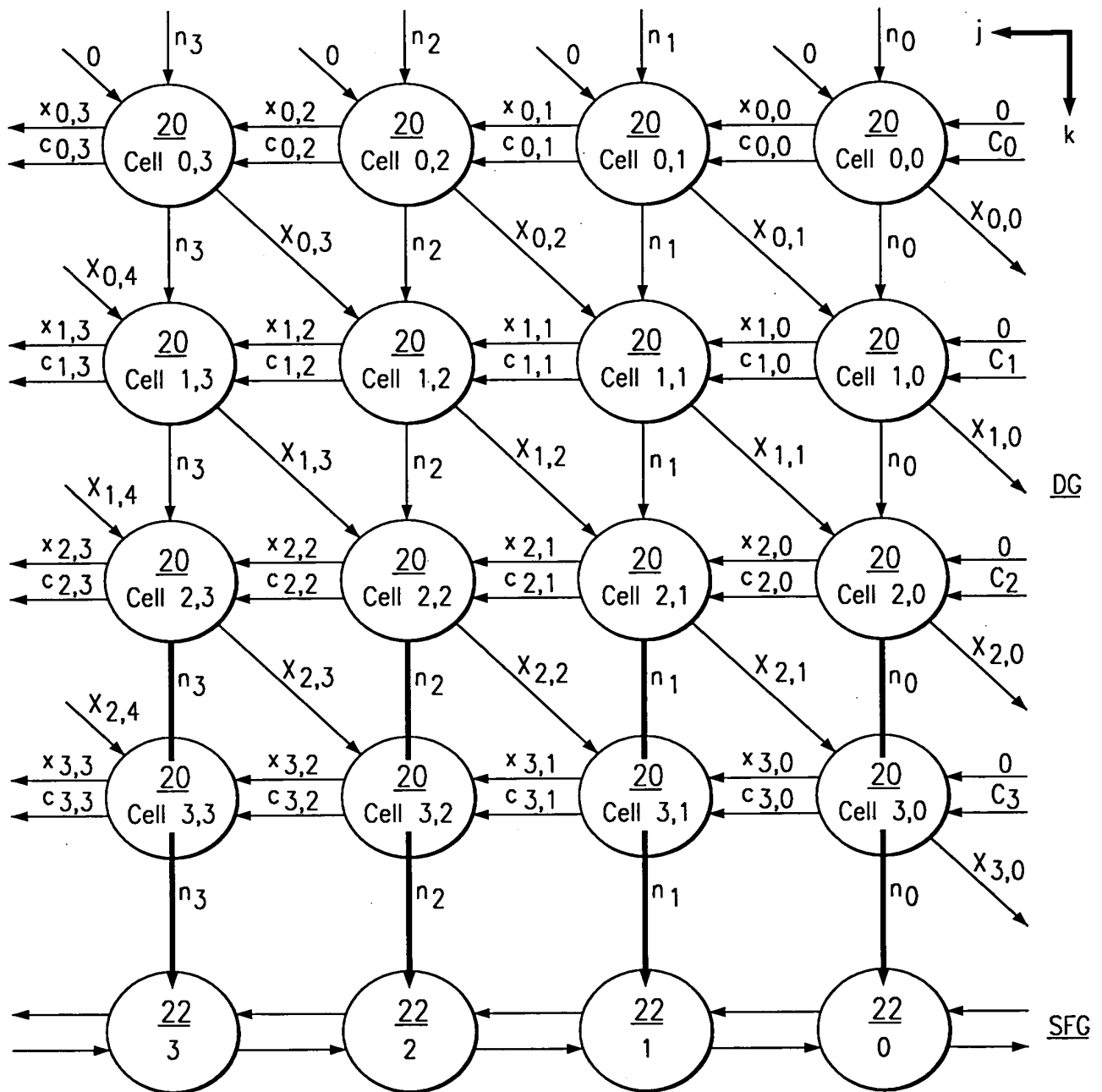


FIG. 12

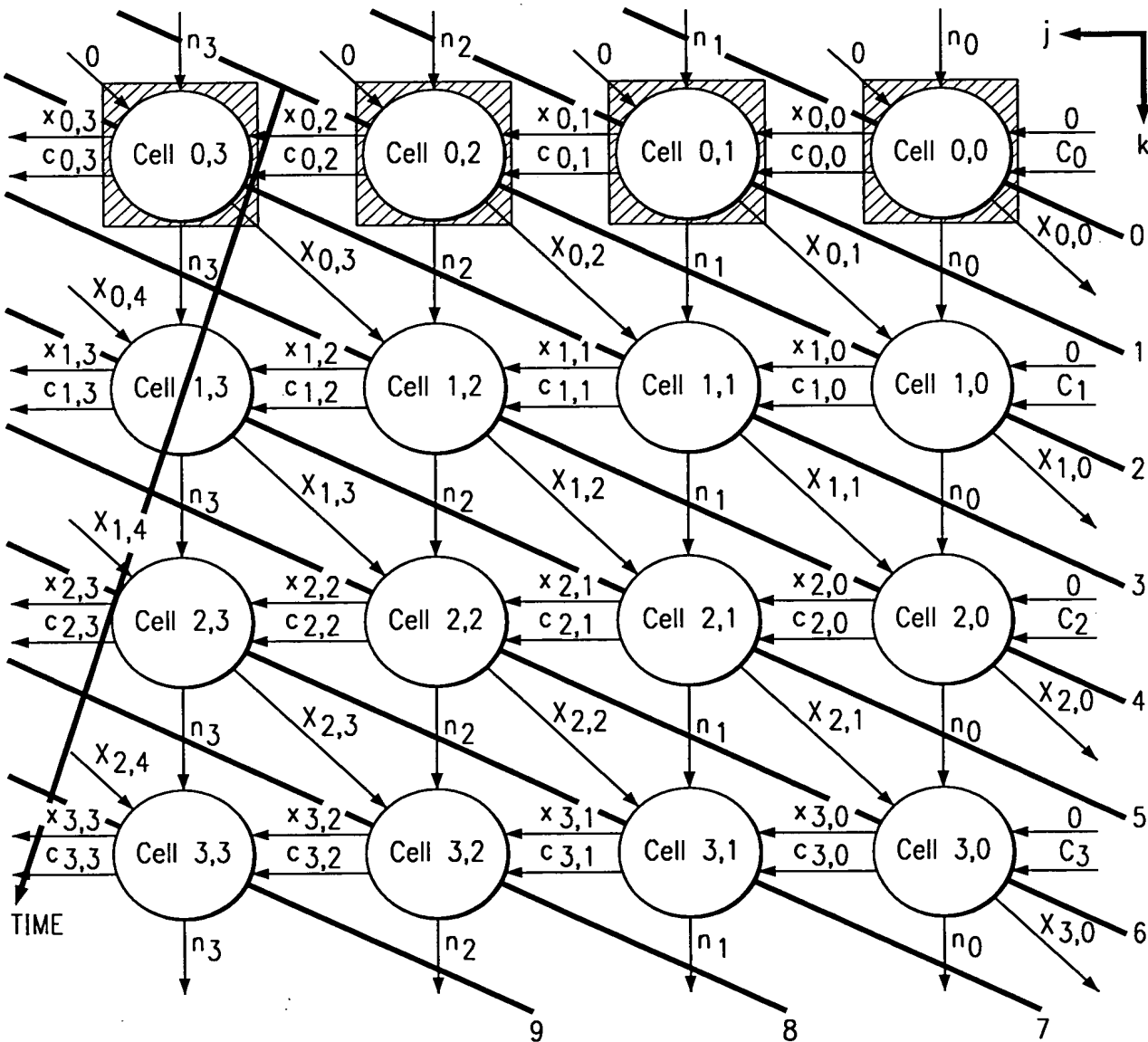


FIG. 13

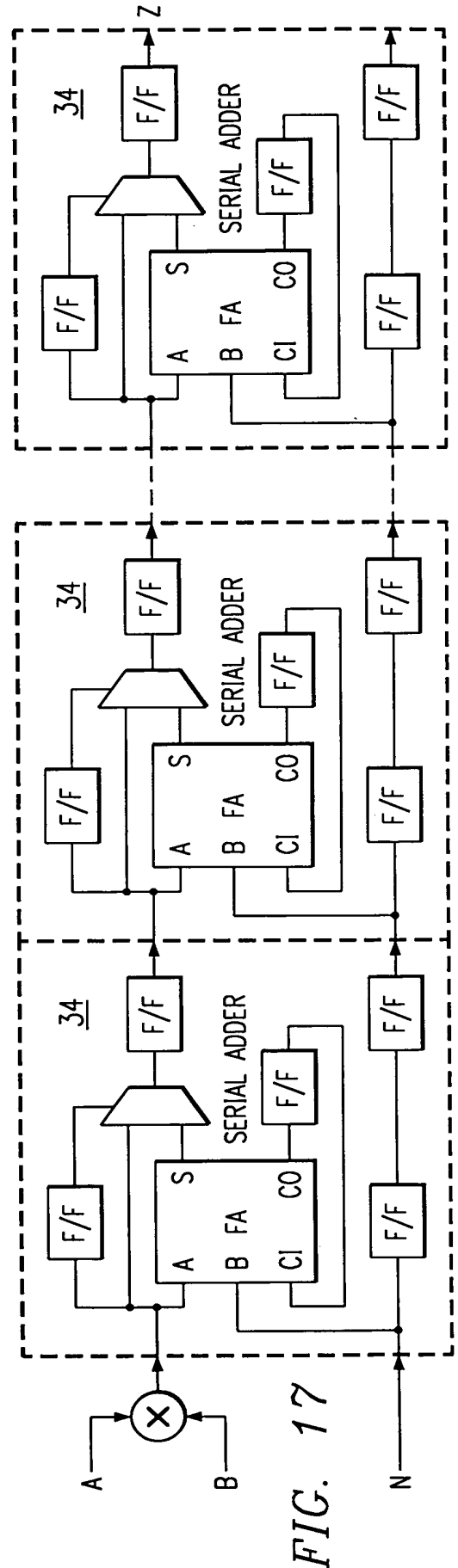
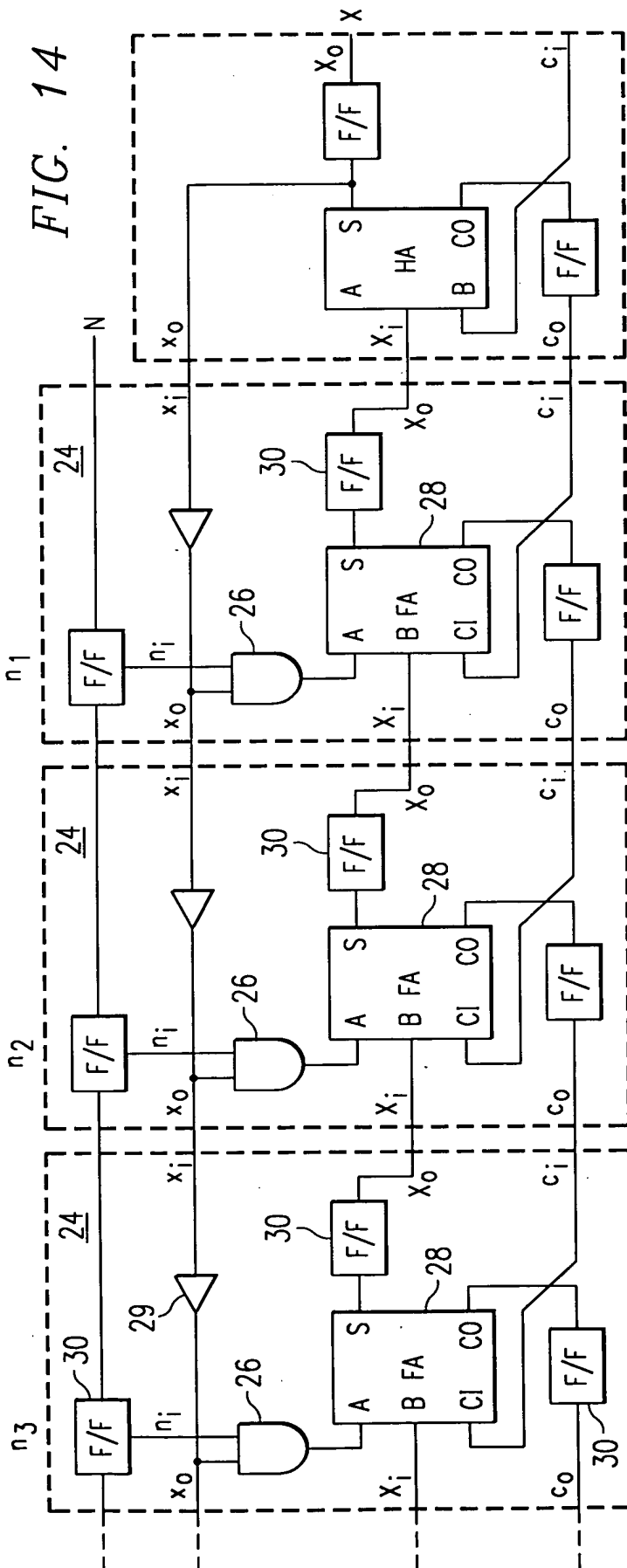
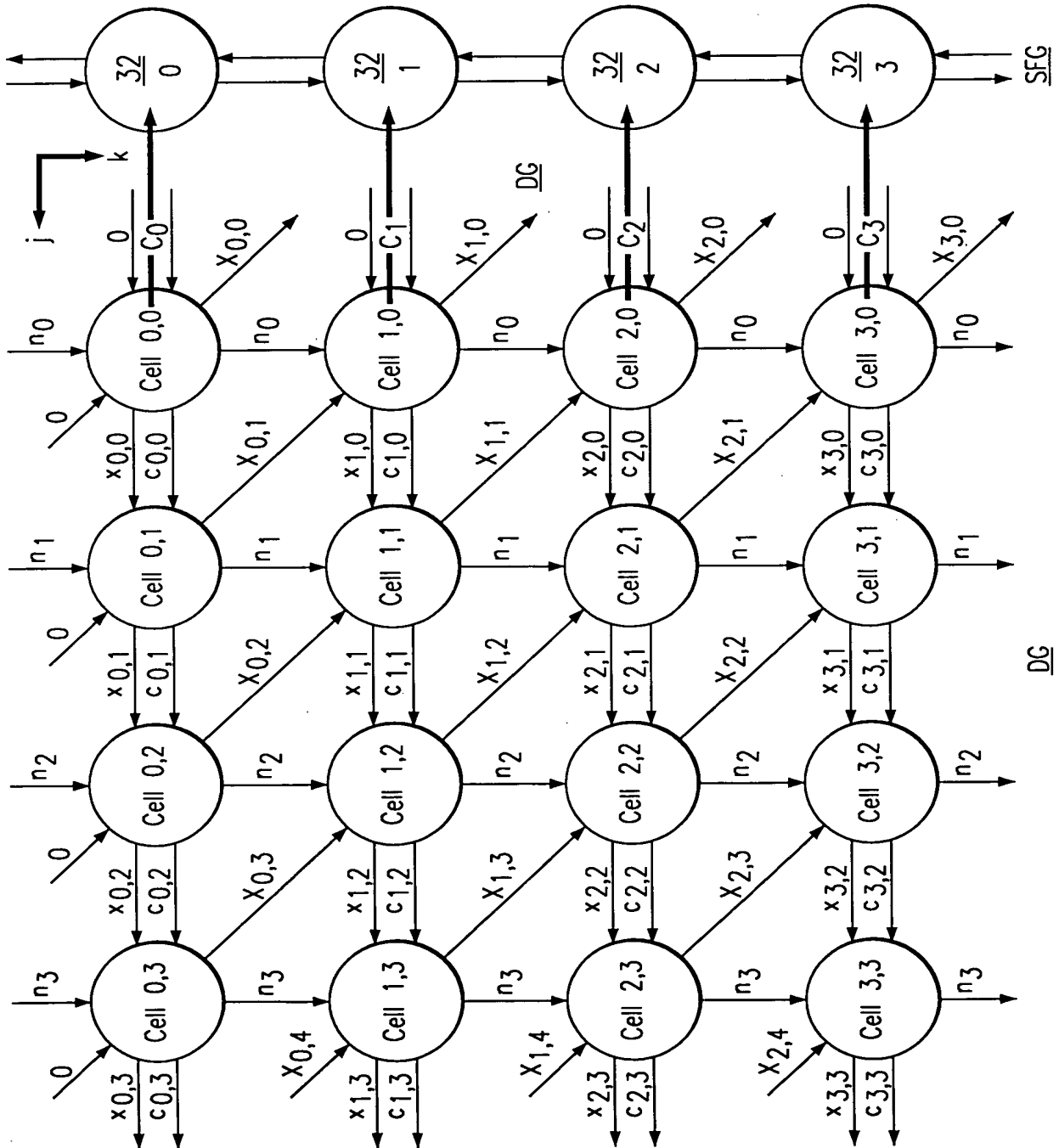


FIG. 15



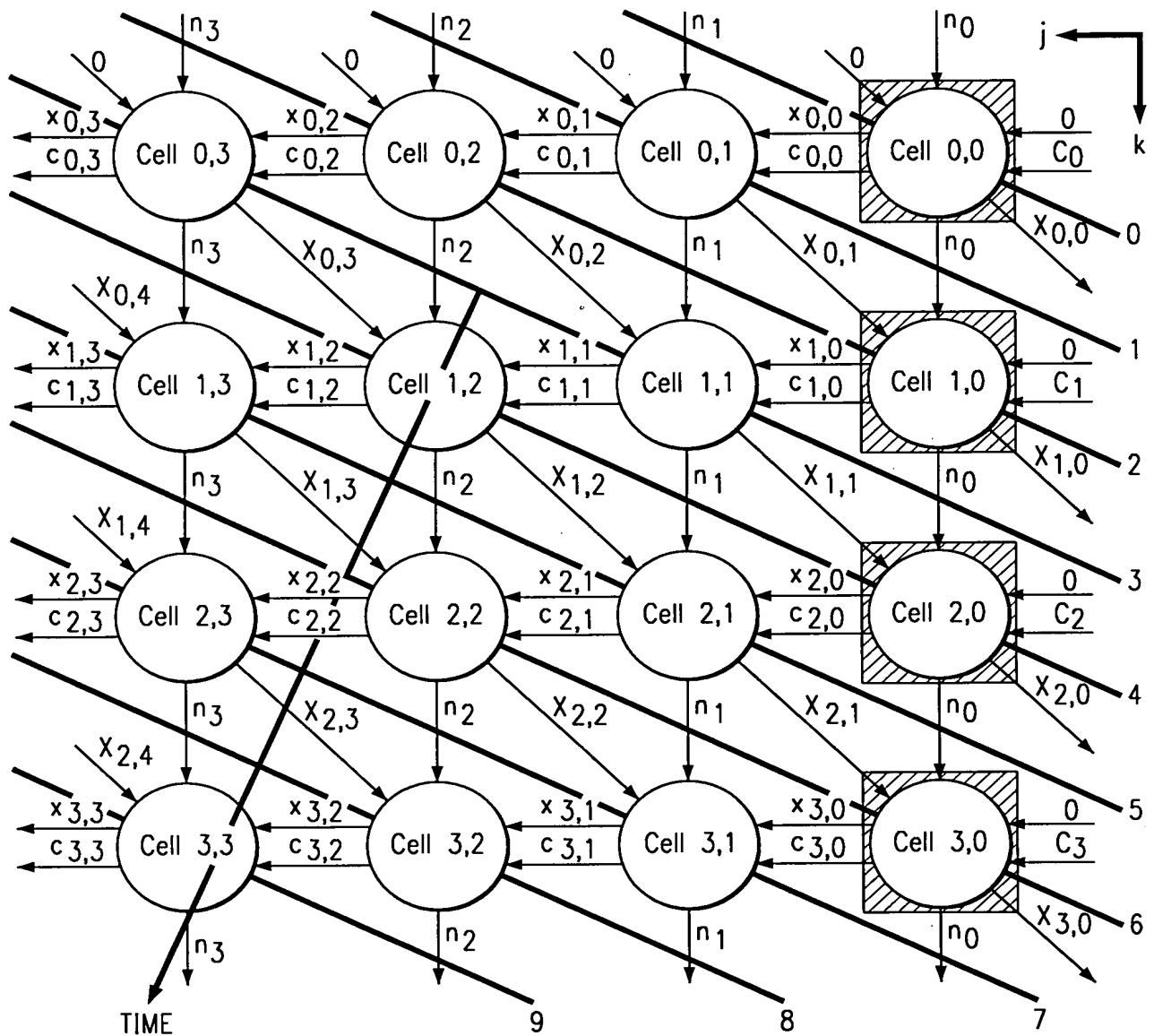
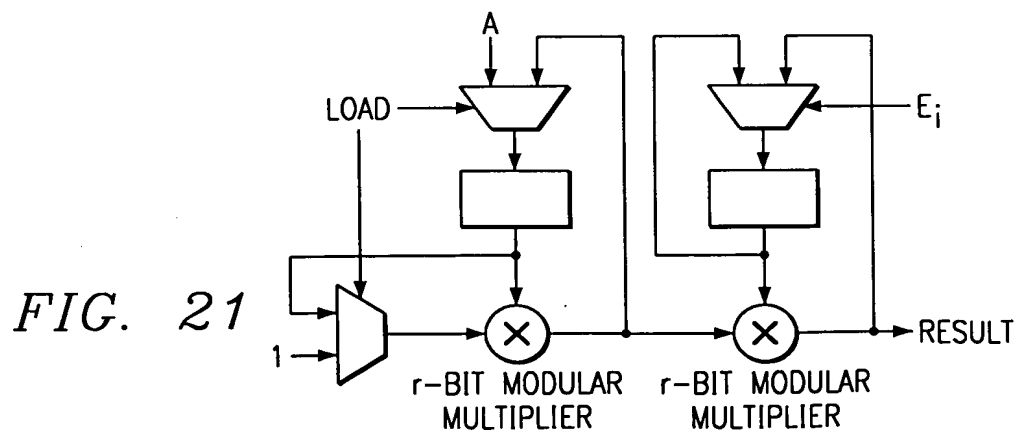
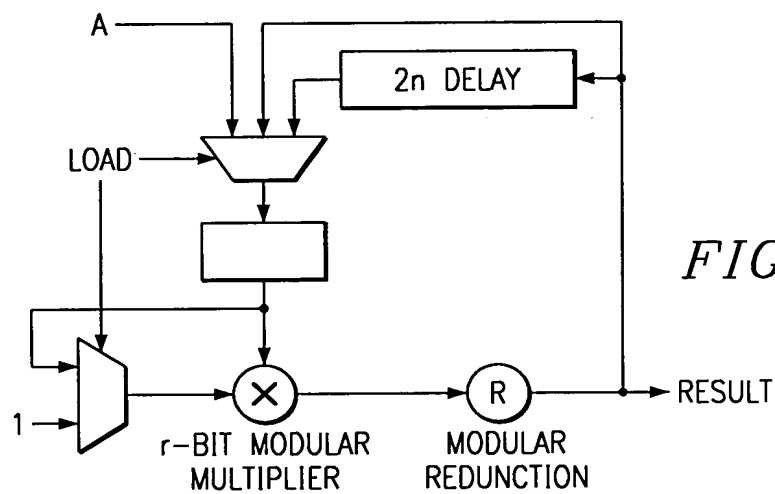
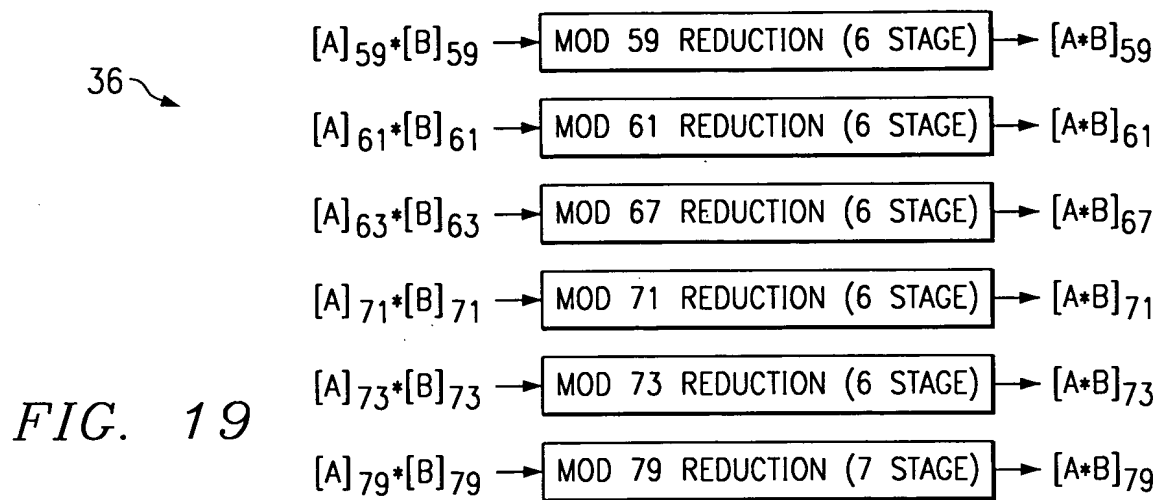


FIG. 16



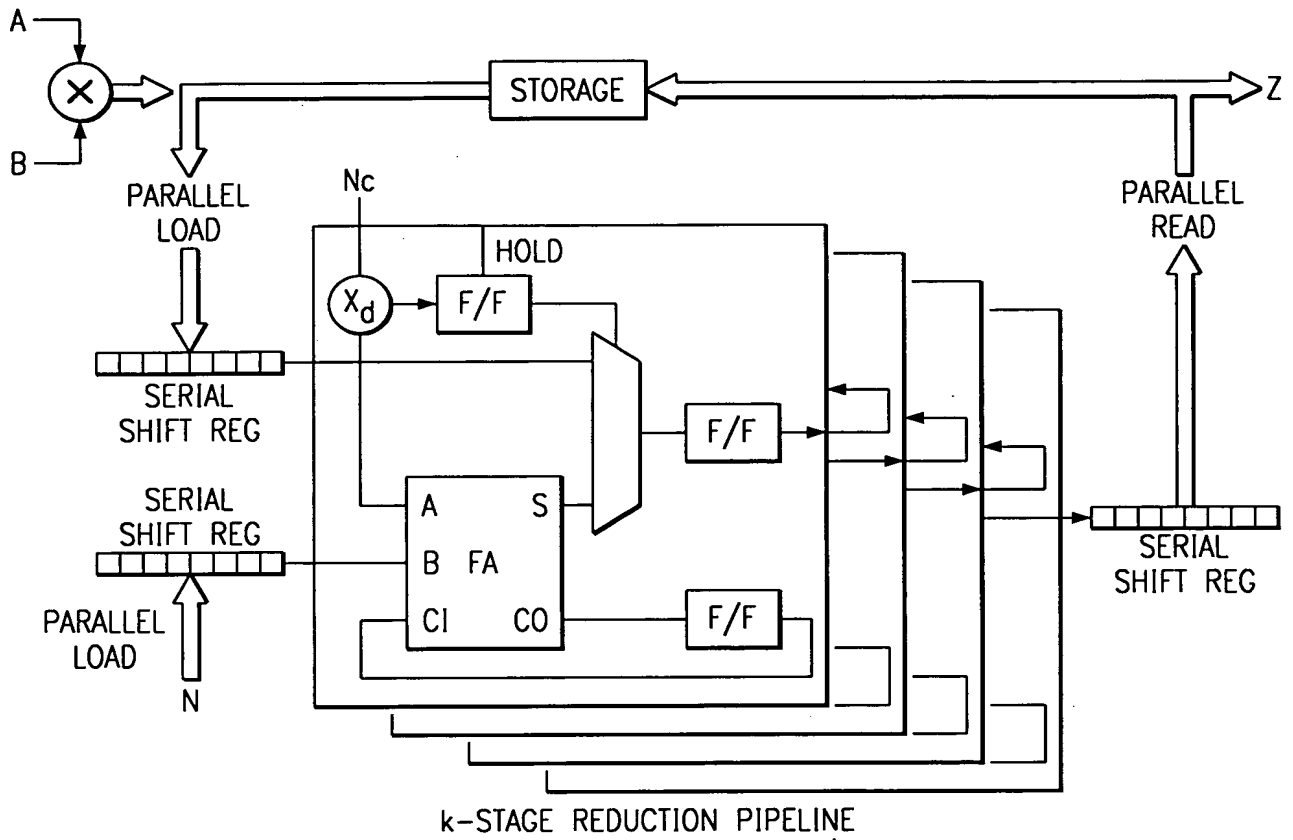


FIG. 22

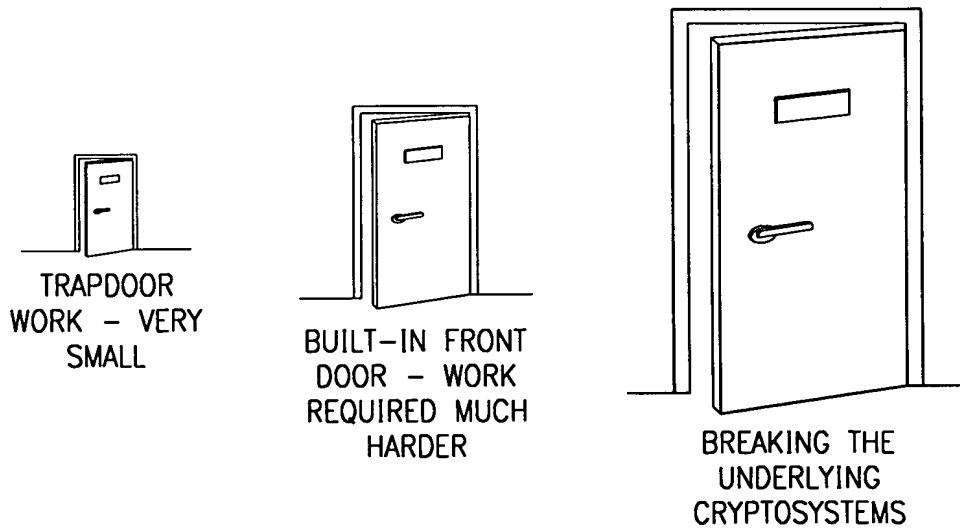


FIG. 23

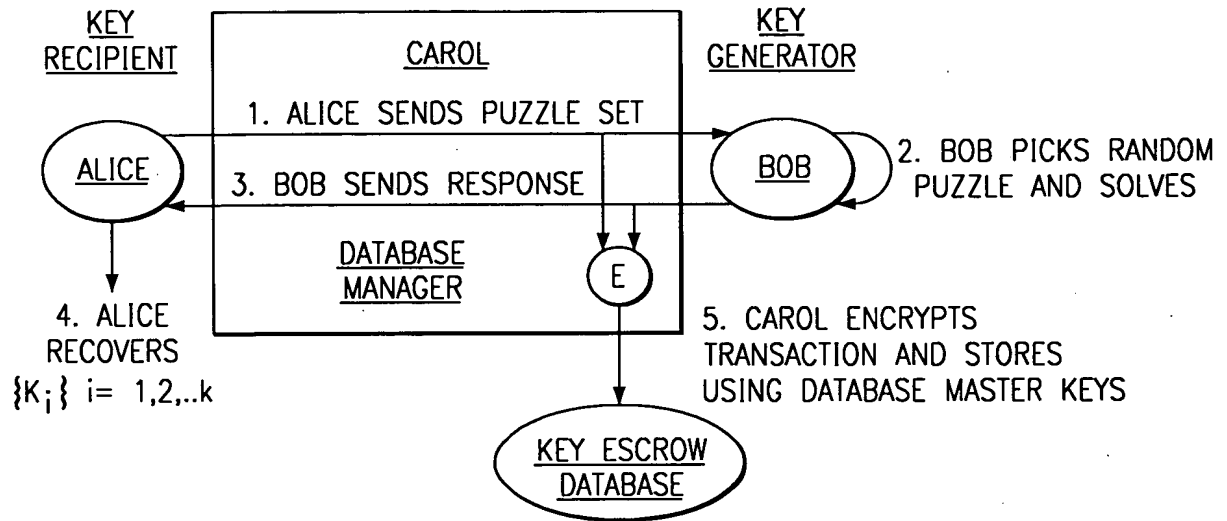


FIG. 24

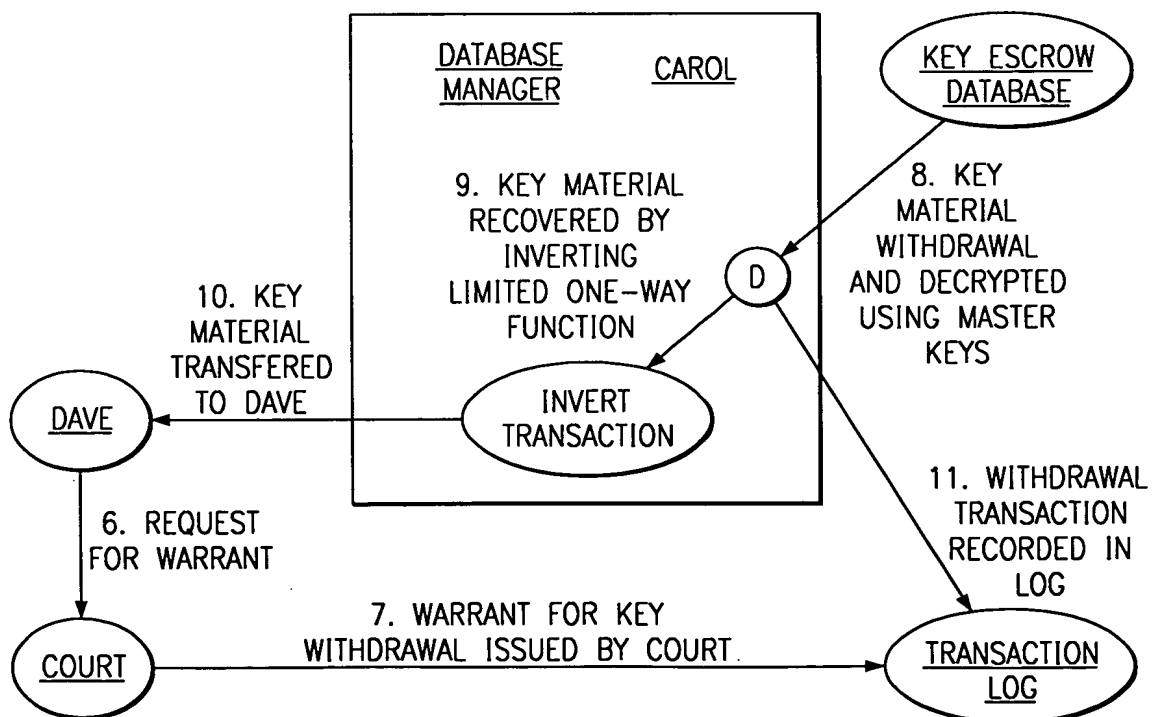


FIG. 25

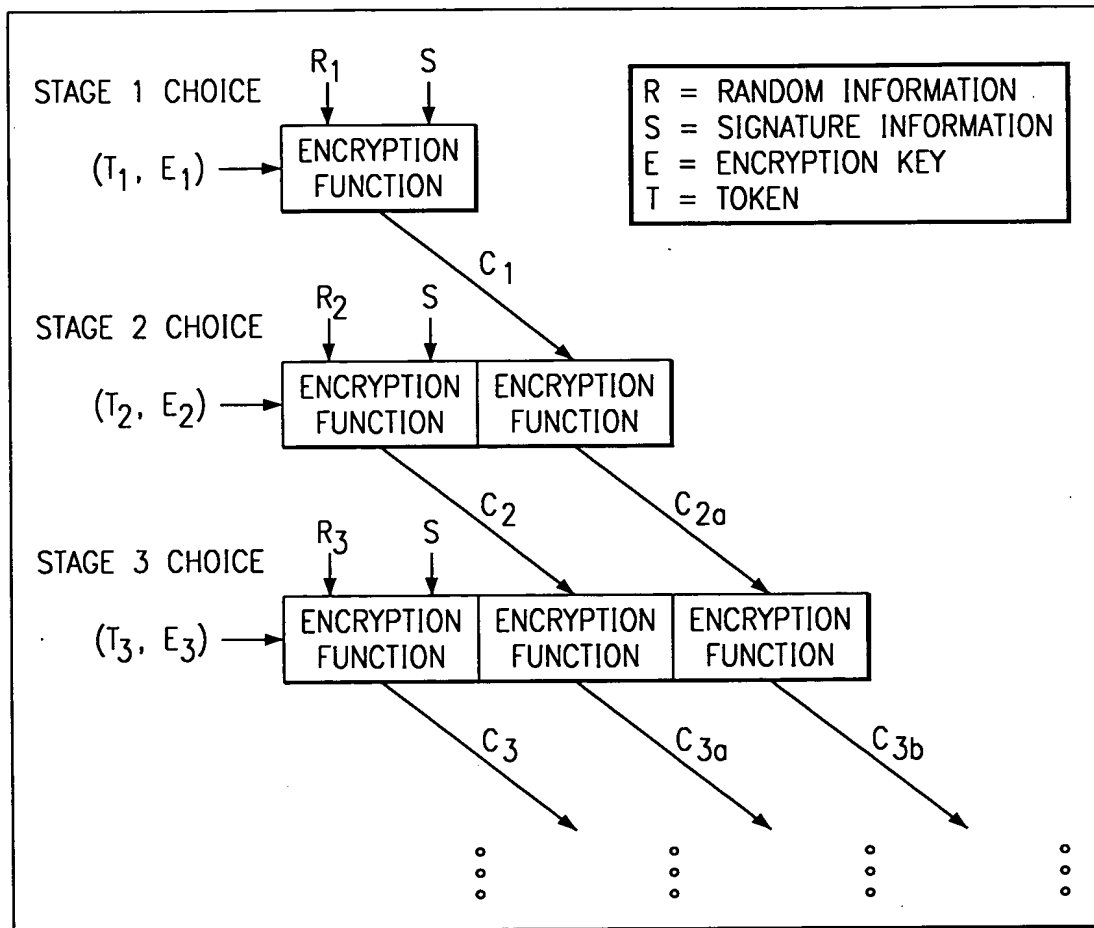


FIG. 26

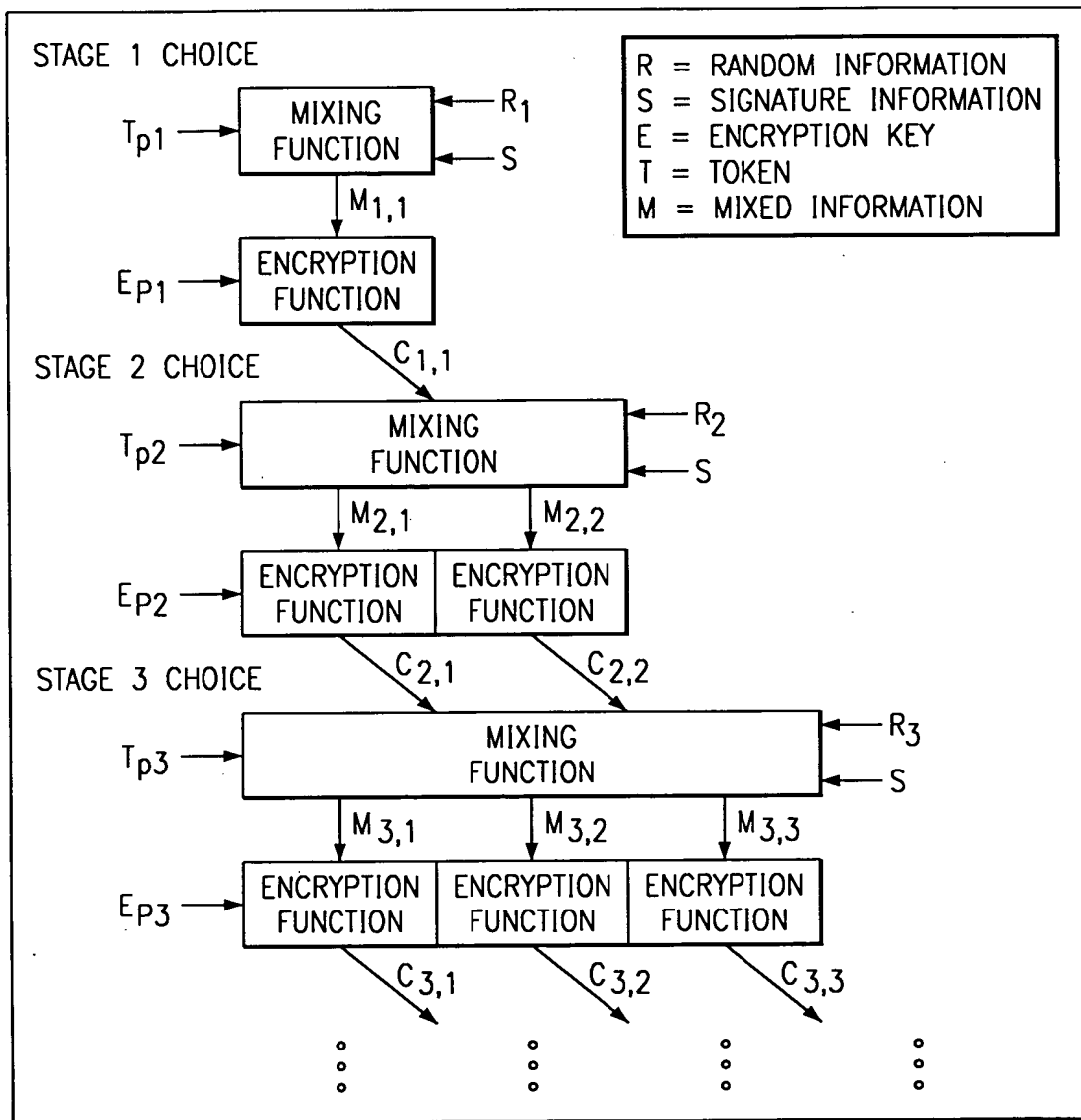


FIG. 27